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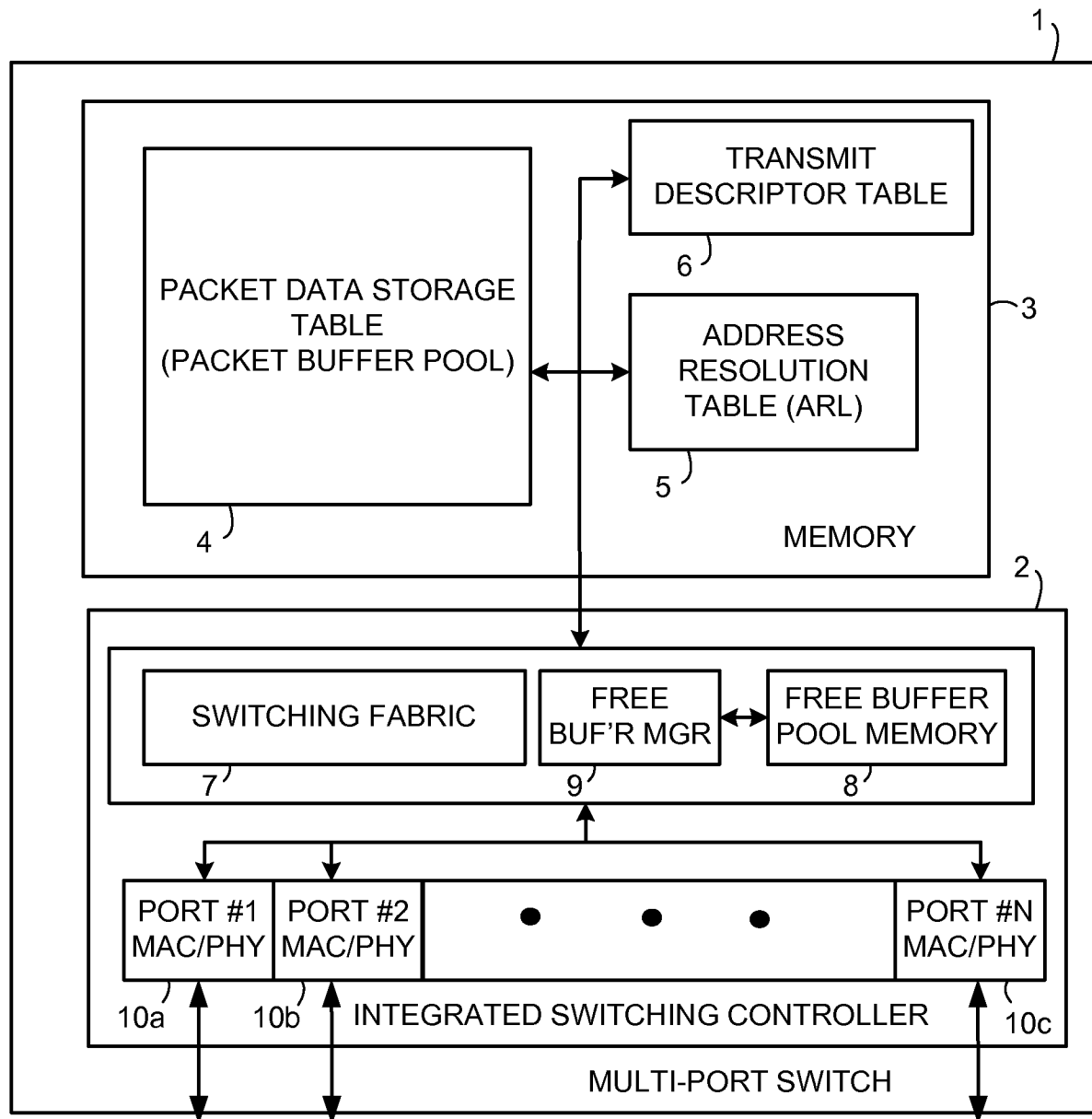


FIG. 1

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11	Address[7:0]	Data[63:0]	
	00-CF	Packet Data	12
13	D0	Port 0 Transmit Descriptor	14
	D1	Port 1 Transmit Descriptor	
	D2	Port 2 Transmit Descriptor	
	D3	Port 3 Transmit Descriptor	
	D4	Port 4 Transmit Descriptor	
	D5	Port 5 Transmit Descriptor	
	D6	Port 6 Transmit Descriptor	
	D7	Port 7 Transmit Descriptor	
15	D8	Port 8 Transmit Descriptor	16
	D9-DF	Unused	
17	E0-FF	32 ARL Table Entries	18

100

FIG. 2

19	Address[16:0]	Data[63:0]	
	0000-00FF	Memory Block 0	20
	0100-01FF	Memory Block 1	
	0200-02FF	Memory Block 2	
	0300-FEFF	Memory Block 3 to 2046	
	FF00-FFFF	Memory Block 2047	
	10000-1FFFF	Optional Memory Block 2048-4095	

FIG. 3

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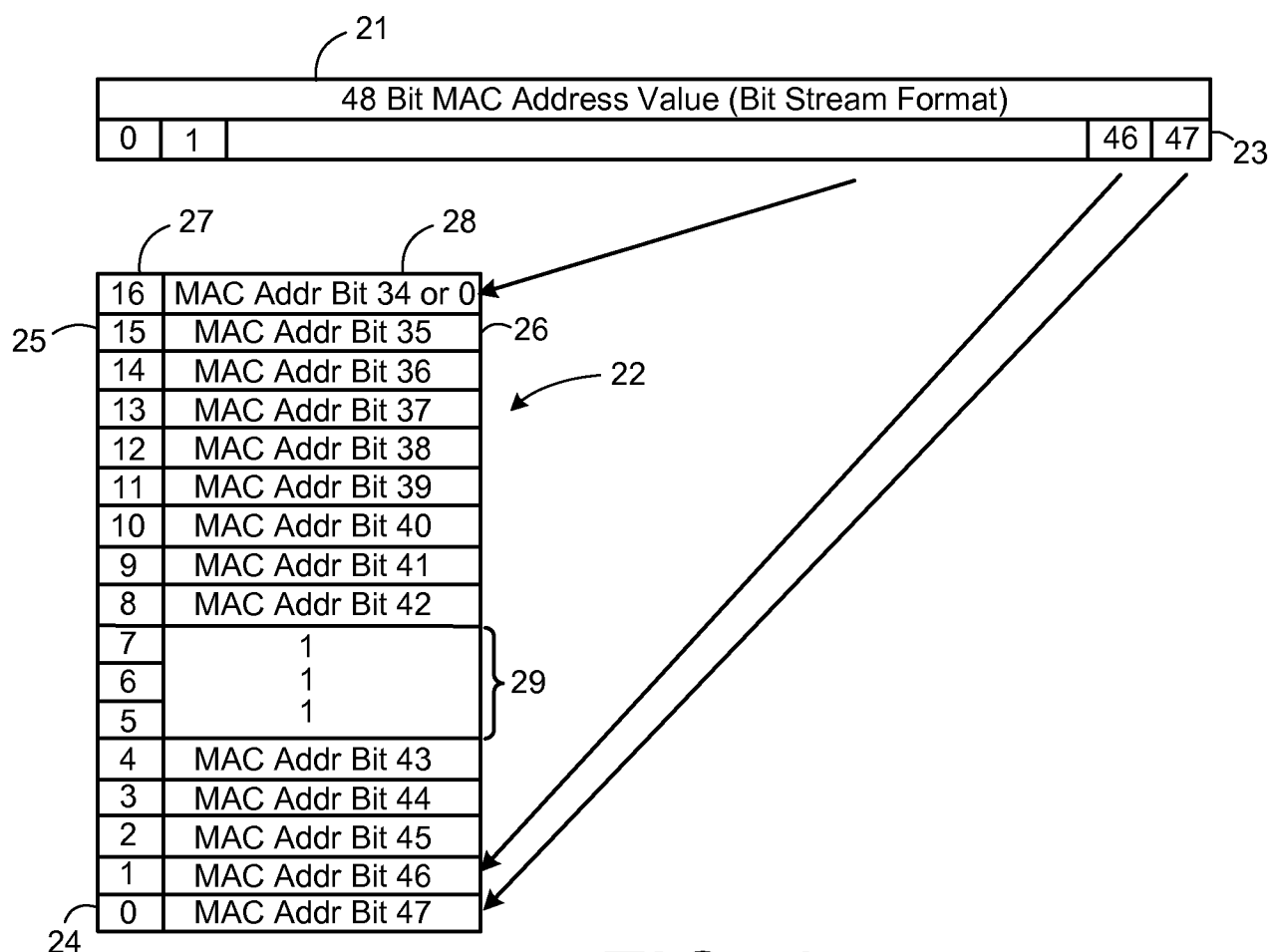


FIG. 4

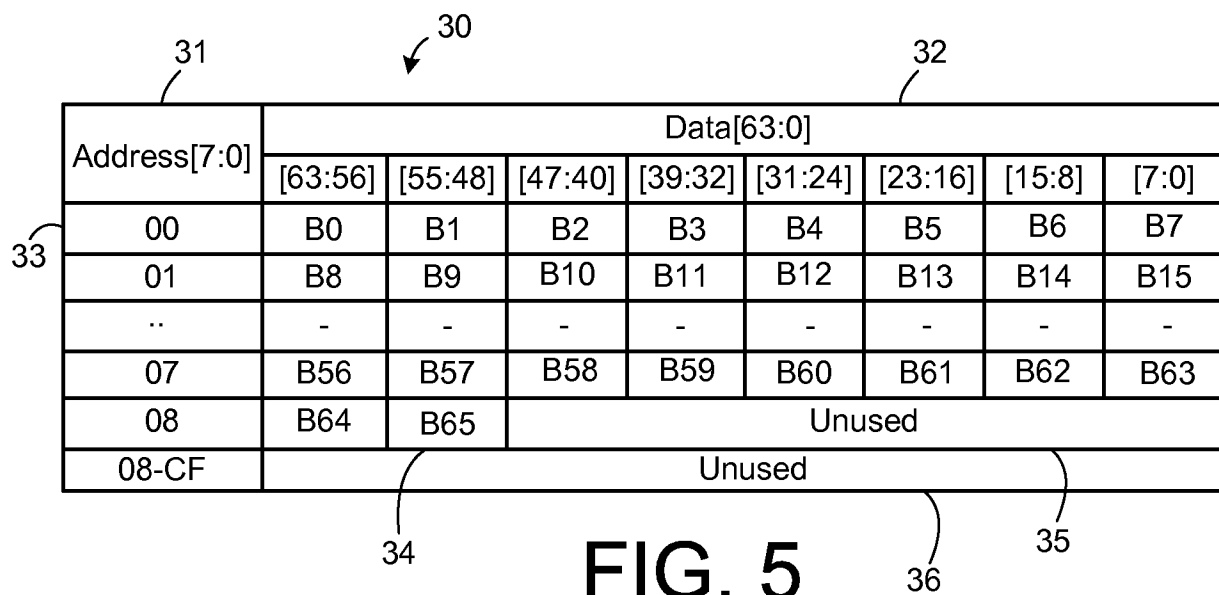


FIG. 5

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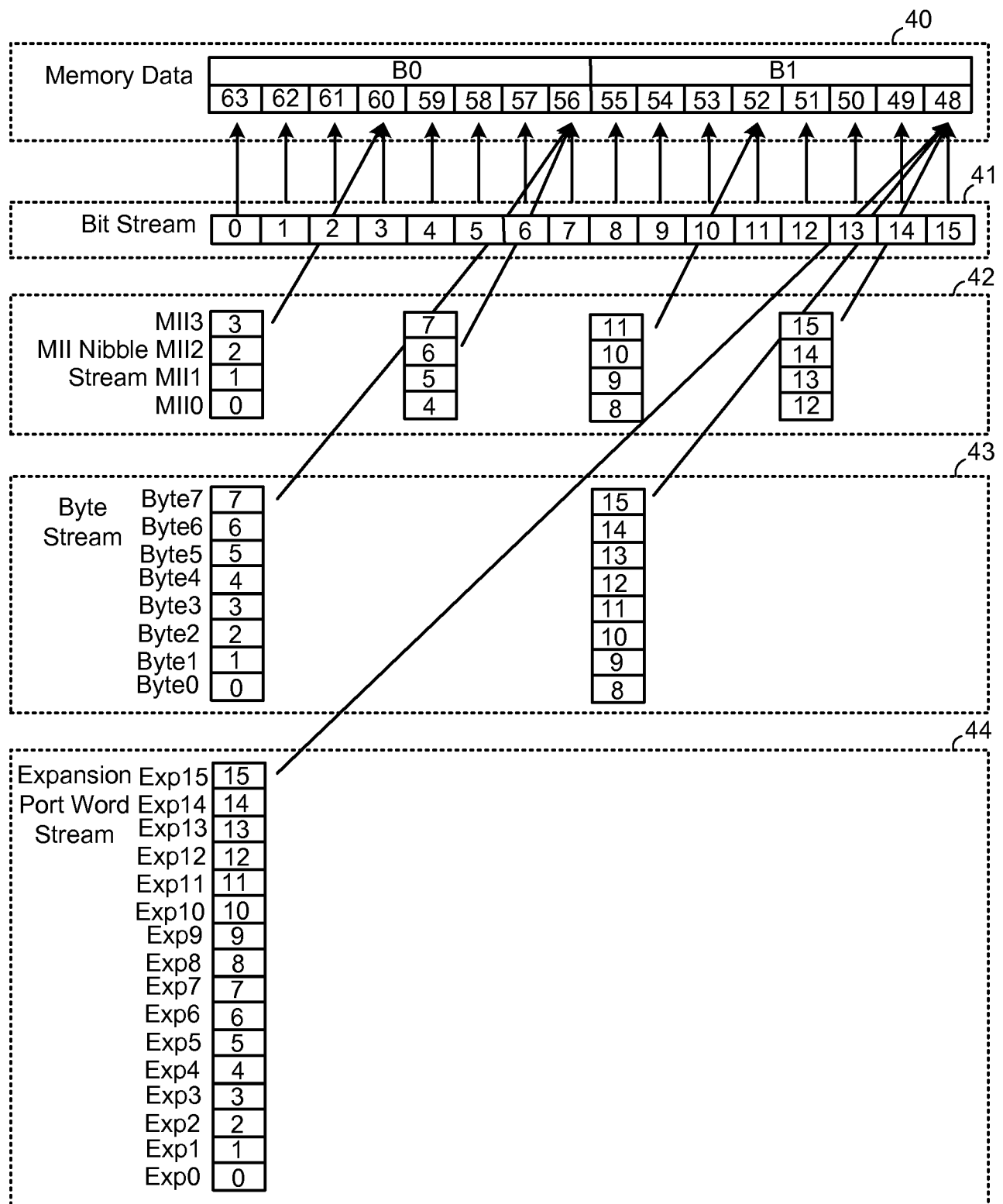


FIG. 6

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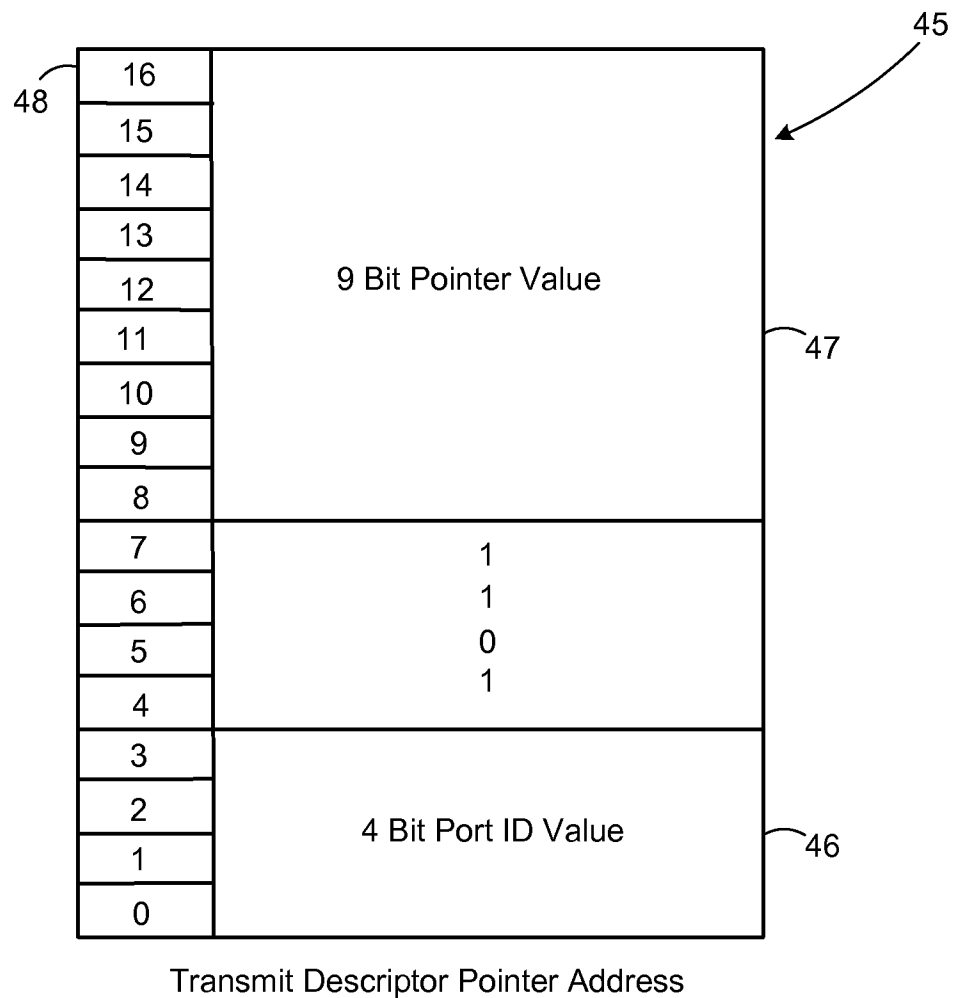
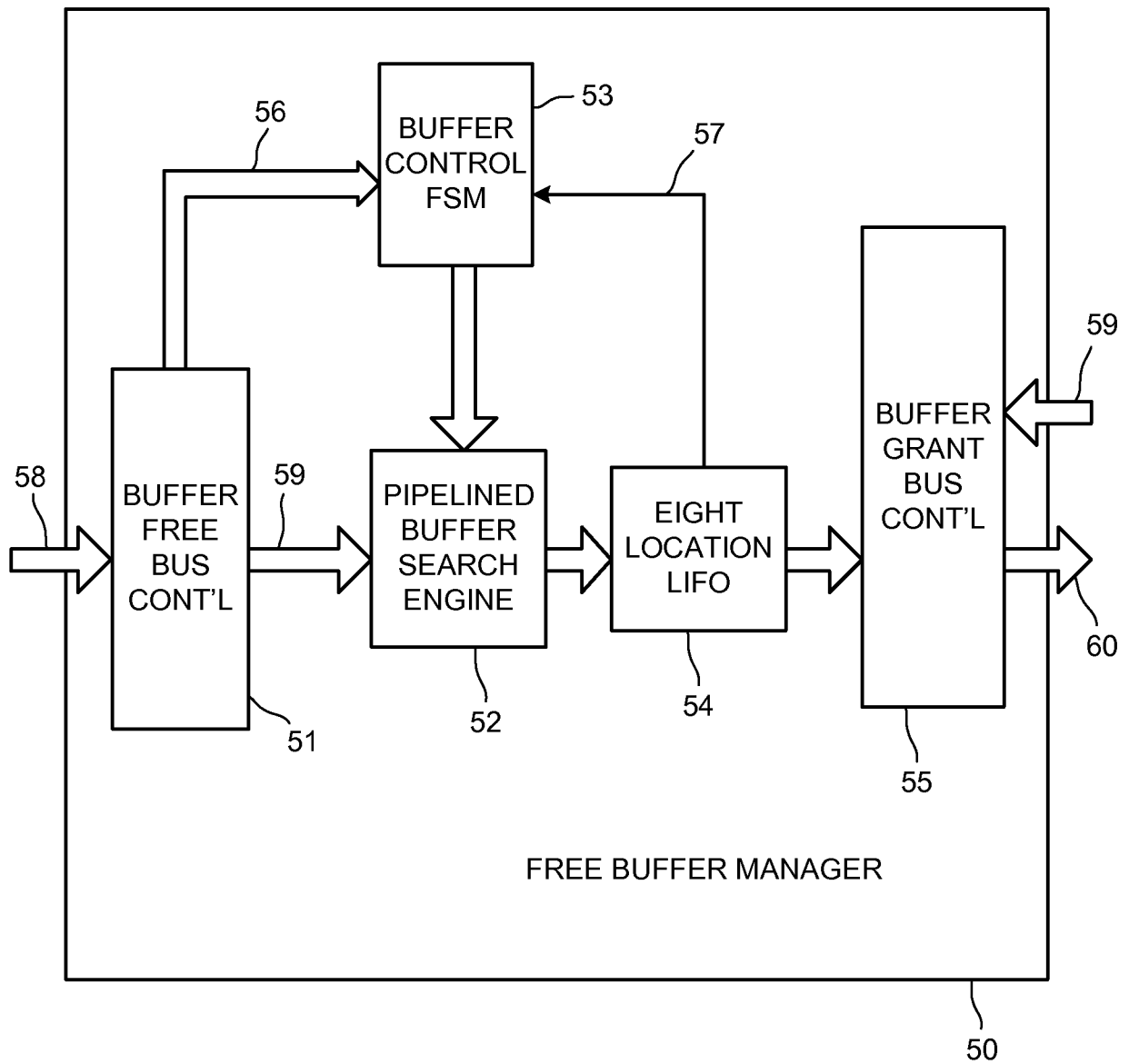
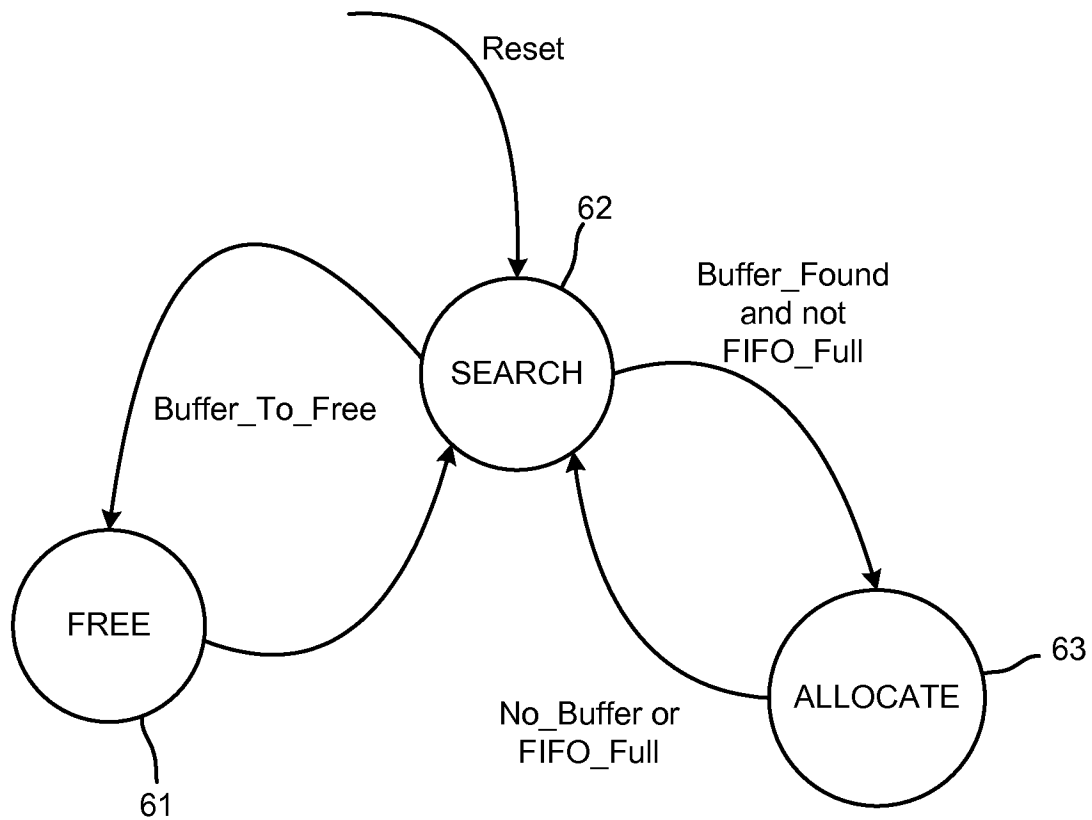


FIG. 7

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**FIG. 8**

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**FIG. 9**